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STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Scott P. Campbell

Application No./Patent No.: 10/715,555 Filed/Issue Date: November 19, 2003

Entitled: CONTOURED SURFACE COVER PLATE FOR IMAGE SENSOR ARRAY

Micron Technology, Inc., a Corporation
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest.
 (The extent (by percentage) of its ownership interest is 100 %)

in the patent application/patent identified above by virtue of either:

- A. ☐ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at _____, Frame _____, or for which a copy thereof is attached.

OR

- B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

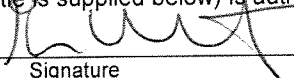
1. From: Scott Campbell To: Photobit Corporation
 The document was recorded in the United States Patent and Trademark Office at Reel 010735, Frame 0035, or for which a copy thereof is attached.
2. From: Photobit Corporation To: Micron Technology, Inc.
 The document was recorded in the United States Patent and Trademark Office at Reel 012745, Frame 0385, or for which a copy thereof is attached.
3. From: Photobit Corporation To: Micron Technology, Inc.
 The document was recorded in the United States Patent and Trademark Office at Reel 014007, Frame 0590, or for which a copy thereof is attached.
4. From: Micron Technology, Inc. To: Aptina Imaging Corporation
 The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

- ☒ As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.


 Signature

1-12-09
 Date

Gianni Minutoli
 Printed or Typed Name

(202) 420-3191
 Telephone Number

Attorney for Assignee
 Title

PATENT ASSIGNMENT AGREEMENT

1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)

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(f)

A large rectangular area of the document is completely redacted with black ink, covering approximately six lines of text.

(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k) [REDACTED]

2. [REDACTED]

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c) [REDACTED]

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, [REDACTED], Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

3

[REDACTED]

4.

[REDACTED]

5.

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: [Signature]

Date: 9/26/08, 2008

Notarization

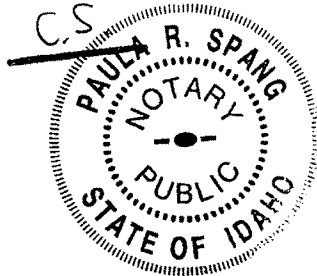
State of Idaho
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Steven B. Appleton
(date) (notary name)

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]
My commission expires: 8/12/11

REVIEWED
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: Thomas L. Lauer, Jr.

Date: 9/26/08, 2008

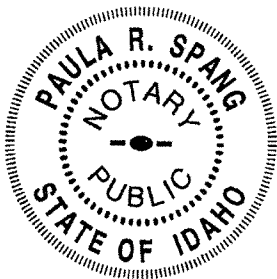
Notarization

State of Idaho
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas L. Lauer, Jr.
(date) (notary name)

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]
My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

ATTACHMENT "B"
IMAGING PATENT APPLICATIONS

Aptina Applications

FILE NUMBER	TITLE	STATUS	COUNTRY NAME	APPLICATION NUMBER	DATE FILED
	SENSORS				
PBIT-0063.00/PC	STITCHED CIRCUITS LARGER THAN THE MAXIMUM RETICLE SIZE IN SUB-MICRON PROCESS	Abandoned	PCT	pclus0006231	Mar 10, 2000
PBIT-0063.00/PR	Circuits larger than the max. reticle size in deep sub micron process	Abandoned	United States of America	60123582	Mar 10, 1999
PBIT-0066.00/PC	CONTOURED SURFACE COVER PLATE FOR IMAGE SENSOR ARRAY	Abandoned	PCT	pclus9930654	Dec 23, 1999
PBIT-0066.00/PR	Contoured Surface of Image Plane Array Cover Plate	Abandoned	United States of America	60113850	Dec 24, 1998
PBIT-0066.01/US	CONTOURED SURFACE COVER PLATE FOR IMAGE SENSOR ARRAY	Pending	United States of America	10715555	Nov 19, 2003
PBIT-0067.00/PR	Backside Illumination of CMOS Image Sensors	Abandoned	United States of America	60116144	Jan 14, 1999
PBIT-0068.00/PR	A Technique for Flagging Oversaturated Pixels	Abandoned	United States of America	60120423	Feb 16, 1999
PBIT-0068.01/US	TECHNIQUE FOR FLAGGING OVERSATURATED PIXELS	Pending	United States of America	11043247	Jan 27, 2005
PBIT-0069.00/PR	DIAGONALIZED IMAGE SENSOR PIXELS FOR IMPROVED EFFECTIVE PERFORMANCE	Abandoned	United States of America	60120805	Feb 19, 1999
PBIT-0072.00/PR	Superposed Multi-Junction Color APS	Abandoned	United States of America	60124084	Mar 9, 1999
PBIT-0073.00/PR	Multi Junction APS with Dual Simultaneous Integration	Abandoned	United States of America	60124153	Mar 8, 1999
PBIT-0073.02/US	Multi Junction APS with Dual Simultaneous Integration	Pending	United States of America	12081660	Apr 17, 2008
PBIT-0074.00/PR	A novel idea for a new readout structure of APS	Abandoned	United States of America	60139348	Jun 15, 1999
PBIT-0074.01/US	ACTIVE PIXEL SENSOR (APS) READOUT STRUCTURE WITH AMPLIFICATION	Pending	United States of America	11022945	Dec 28, 2004
PBIT-0078.00/PR	A Method of Increasing Pixel Conversion Gain	Abandoned	United States of America	60130998	Apr 23, 1999